



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No.	10/767,751	Confirmation No.	5740
First Inventor	Richard K. Williams	Filing Date	01/28/2004
Tech. Center/ Art Unit	2812	Examiner	Matthew E. Warren
Title:	Modular Bipolar-CMOS-DMOS Analog Integrated Circuit And Power Technology		
Docket No.:	AAT007-4C US	Customer No.:	34036

Santa Clara, California  
November 7, 2005

MAIL STOP AMENDMENT  
COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, VA 22313-1450

**AMENDMENT AND RESPONSE TO ELECTION REQUIREMENT**

Dear Sir:

In response to the Office Action dated August 24, 2005, please amend the above-identified application as follows.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks/Arguments** begin on page 6 of this paper.